

Attorney Docket No. 1614.1069

Co/C

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Megumi YOKOI et al.

Application No.: 09/645,880

ATTENTION

Confirmation No.: 4079

CERTIFICATE OF CORRECTION

Filed: August 25, 2000

BRANCH

U.S. Patent No.: 6,880,046

Issued: April 12, 2005

For: MULTIPROCESSOR SYSTEM AND MEMORY ACCESS METHOD

**REQUEST FOR CERTIFICATE OF CORRECTION**

Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Sir:

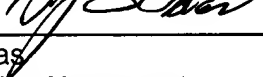
Patentee(s) respectfully request(s) that a Certificate of Correction be issued in the subject patent, pursuant to 35 U.S.C. §254 and 37 C.F.R. §1.322, to correct the mistake(s) shown on the attached Form PTO-1050.

Since the mistakes are Patent Office mistakes, it is believed that no fee is required.

Respectfully submitted,

STAAS & HALSEY LLP

Date: Nov. 15, 2005

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Certificate  
NOV 18 2005  
of Correction

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(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE  
CERTIFICATE OF CORRECTION

PATENT NO : 6,880,046

DATED : April 12, 2005

INVENTOR(S) : Megumi YOKOI et al.

Column 11, line 20, change";" to --,--;  
Column 11, line 40, after "claim 7" insert --,--;  
Column 11, line 50, after "claim 7" insert --,--;  
Column 12, line 17, after "claim 13" insert --,--.

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PATENT NO. 6,880,046

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